	Туре	L#	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	19028	pipeline\$1 same (processor microprocessor mpu cpu)		2006/10/27 21:40
2	BRS	L2	23006	power adj2 controller	I	2006/10/27 21:40
3	BRS	L3	160	1 and 2	100	2006/10/27 21:40
4	BRS	L4	8983	model\$3 near5 capacit\$4		2006/10/27 21:41
5	BRS	L5	301	maximum adj average adj power	US-PGPUB; USPAT	2006/10/27 21:57
6	BRS	L6	6	4 and 5	US-PGPUB; USPAT	2006/10/27 21:42
7	BRS	L7	18983	average adj power	US-PGPUB; USPAT	2006/10/27 21:57
8	BRS	Ľ8	301	5 same 7	US-PGPUB; USPAT	2006/10/27 21:58
9	BRS .	L9	11	4 same 7	US-PGPUB; USPAT	2006/10/27 21:58
10	BRS	L10	8 .	9 not 6		2006/10/27 22:03
11	BRS	L11	3	3 and 4	US-PGPUB; USPAT	2006/10/27 22:06
12	BRS	L12	462	throttl\$3 near5 instruction	US-PGPUB; USPAT	2006/10/27 22:07
13	BRS	L13	6997	(instruction issu\$3) near3 rate		2006/10/27 22:08
14	BRS	L14	20	3 and (12 or 13)	US-PGPUB; USPAT	2006/10/27 22:45
15	BRS	L15	142901	(cool\$3 fan) same (power processor)		2006/10/27 22:46
16	BRS	L16	67	3 and 15		2006/10/27 22:47
17	BRS	L17	28	16 and model	US-PGPUB; USPAT	2006/10/27 22:49
18	BRS	L18	25	17 not 11	US-PGPUB; USPAT	2006/10/27 23:10
19	BRS	L19	2590	stall\$3 near3 instruction		2006/10/27 23:11
20	BRS	L20	458664	threshold	US-PGPUB; USPAT	2006/10/27 23:11
21	BRS	L21	5	3 and 19 and 20	US-PGPUB; USPAT	2006/10/27 23:13
22	BRS	L22	6529	low adj power adj (state mode)		2006/10/27 23:14
23	BRS	L23	18	22 with pipeline		2006/10/27 23:27

	Туре	L#	Hits	Search Text	DBs	Time Stamp
24	BRS	L24	30 .	(bleed adj rate) with set\$4	US-PGPUB; USPAT	2006/10/27 23:29
2 5	BRS	L25	1			2006/10/27 23:29
26	BRS	L26	1104		00	2006/10/27 23:30
27	BRS	L27	33667	<u> </u>		2006/10/27 23:31
28	BRS	L28	2	26 same 27	US-PGPUB; USPAT	2006/10/27 23:32
29	BRS	L29	20	("4644466" "5452215" "5521834" "5557531" "5559458" "5666506" "5684422" "5751984" "5859999" "5860017" "5884061" "5898235" "5903768" "5938755" "5941991" "6122728" "6195756" "6275928" "6357016" "6367023").PN.	US-PGPUB;	2006/10/27 23:33
30	BRS	L30	1	3 and 29	US-PGPUB; USPAT	2006/10/27 23:33